

1 1. A system interface, comprising
2 a packet switching network;
3 a cache memory;
4 a plurality of directors, one portion of such directors being adapted for coupling to
5 a host computer/server and another portion of the directors being adapted for coupling to
6 a bank of disk drives; and wherein the plurality of directors and cache memory are
7 interconnected through the packet switching network.

1 2. The system interface recited in claim 1 wherein the cache memory includes a
2 plurality of cache memory sections, each one of such sections being coupled to a
3 corresponding one of the crossbar switches.